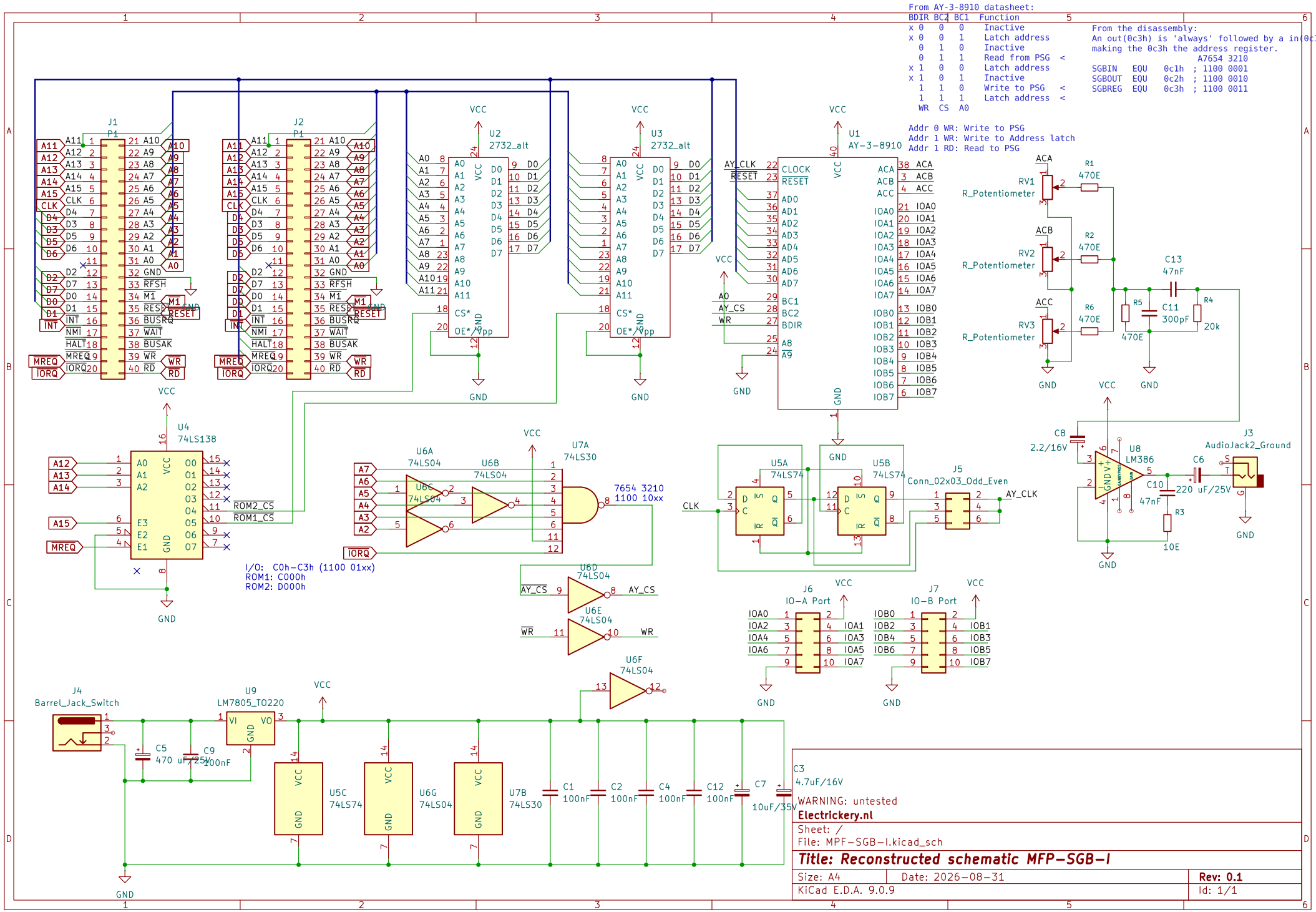
From AY-3-8910 datasheet:
BDIR BC2 BC1 Function
x 0 0 0 Inactive
x 0 0 1 Latch address
0 1 0 Inactive
0 1 1 Read from PSG <
x 1 0 0 Latch address
x 1 0 1 Inactive
1 1 0 Write to PSG <
1 1 1 Latch address <
WR CS A0
From the disassembly:
An out(0c3h) is 'always' followed by a in(0c1h) or ou
making the 0c3h the address register.
A7654 3210
SGBIN EQU 0c1h ; 1100 0001
SGBOUT EQU 0c2h ; 1100 0010
SGBREG EQU 0c3h ; 1100 0011
Addr 0 WR: Write to PSG
Addr 1 WR: Write to Address latch
Addr 1 RD: Read to PSG
J1
P1
J2
P1
U1
AY-3-8910
U2
2732_alt
U3
2732_alt
U4
74LS138
U5A
74LS74
U5B
74LS74
U5C
74LS74
U6A
74LS04
U6B
74LS04
U6C
74LS04
U6D
74LS04
U6E
74LS04
U6F
74LS04
U6G
74LS04
U7A
74LS30
U7B
74LS30
U8
LM386
U9
LM7805_TO220
J3
AudioJack2_Ground
J4
Barrel_Jack_Switch
J5
Conn_02x03_Odd_Even
J6
IO-A Port
J7
IO-B Port
R1
470E
R2
470E
R5
470E
R6
470E
RV1
R_Potentiometer
RV2
R_Potentiometer
RV3
R_Potentiometer
R4
20k
R3
10E
C13
47nF
C11
300pF
C6
220 uF/25V
C10
47nF
C8
2.2/16V
C5
470 uF/25V
C9
100nF
C1
100nF
C2
100nF
C4
100nF
C12
100nF
C7
10uF/35V
C3
4.7uF/16V
I/O: C0h-C3h (1100 01xx)
ROM1: C000h
ROM2: D000h
7654 3210
1100 10xx
WARNING: untested
Electrickery.nl
Sheet: /
File: MPF-SGB-I.kicad_sch
Title: Reconstructed schematic MFP-SGB-I
Size: A4
Date: 2026-08-31
Rev: 0.1
KiCad E.D.A. 9.0.9
Id: 1/1