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| ENGINEERING SPECIFICATION |  |  |  |  |
| I. general <br> This procedure defines the configurations, module utilization, installation, acceptance and diagnostic startup criteria for (EK-RL01-SV) for drive acceptance procedure. <br> ii. conpiguration <br> A. Setup switches and jumpers as indicated in tables 1 and 2 for type of rom, standard Device Address 174490; and vector Address 15\%. See pigures 2 and 3 for switch and jumper locations. For non standard device and Vector Address <br>  vector switch on for a 1 off for a $a$. <br> iii. modele utilization <br> A. Allowable Backplanes <br> a9273 $4 \times 9$ slot backplane for the BA11-N or 11/83L or an electrical equivalent with LsI-11 bus or the A ard an connectors and $C$, $\begin{aligned} & \text { interconnect bus on the } \\ & C\end{aligned}$ and connectors. <br> в. Module Order <br> Modules are inserted in any two consecutive slots with the M8013 inserted in the slot which is electrically closest to the cpu and the M8814 inserted in the slot directly benind <br> c. System Guidelines <br>  (further away from the cpu) than any volatile devices, tape units, and the RKvil as in figure 1 . |  |  |  |  |
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[^0]| ENGINEERING SPECIFICATION | rivil field installation and acceptance procedure |
| :--- | :--- | :--- | :--- |
| title | continuation sheet |

IV. ACCEPTANCE PROCEDURE
A. Equipment

1. Hardware
a. BAll-N 11/03 system with a minimum of 16 K of memory b. Console terminal (VT50, La36, etc.)
2. Diagnostics
CVRLAAg RLVIl $\frac{\text { Program Name }}{\text { RLO1 Diskless }}$ Test
Maindec
CZRLABQ RLIl/RLV11 Controller Test (Part 1) AC-B107A-MC
C2RLBBG RLIl/RLV11 Controller Test (Part 2) $\begin{array}{llll}A C-E ø 4 @ B-M C\end{array}$
B. Acceptance Criteri

| Program Name | Accept Time |
| :---: | :---: |
| CVRLAAg | 16 Passes |
| CzRLAbg | 5 Passes |
| CzRLbbg | 3 Passes |

v. PROGRAM START PROCEDURE
A. Program Loading

Follow standard DEC procedures for program loading. Absolute oader for paper tape. XXDP, UPDI, UPD2, UPD3 for other media.
B. Program Starting

Program start location is 200. Use standard DEC procedures. For LSI-11 with ODT Type 200 G.
C. Program Execution Procedure

1. Program Example for CVRLAA

After loading and starting program, it will respond a the console with the following. See diagnostic listing for more detailed explanation or if errors are

| SIZE | CODE | NUMBER |  |
| :---: | :---: | :---: | :---: |
| A | SP | RLV1ג- $\not \subset-2$ | REV |

DEC FORM NO EN.01022-16-N370:(381) SHEET 5 OF

## ENGINEERING SPECIFICATION <br> $\qquad$ <br> CONTINUATION SheEt

title rlvil field installaticn and acceptance procedure
2. Program Response Operator Action

CVRLA (Program Name
L-CIK (L) N?
(Type $\begin{gathered}\text { y } \\ \text { with } \\ \text { carriage return } \\ \text { VII } \\ \text { RTC } \\ \text { Enabler }\end{gathered} 1 / \varnothing 3 \mathrm{~L}$
50 HZ (L) N? (Type carriage return if system is
LSI (L) N?

DS-B
\# Units (D) ?
Type carriage return
(Type S T A carriage return
(Type $\underline{1}$ carriage return
RLll (L) Y? (Type $\underline{N}$ carriage return)
Bus Address ( $\varnothing$ ) $1744 \varnothing \varnothing$ ? (Type carriage return)
vector ( $\varnothing$ ) 330 ? (Type 160 carriage return)
BR Level ( $\varnothing$ ) 5? (Type carriage return)
Drive ( $\varnothing$ ) $\varnothing$ ? (Type carriage return
Change SW (L)? (Type N carriage return)
CVRLA EOP 1 (approximately 45 seconds)
Type $C$ (Control/c) to end execution

|  | SIZE  <br> $\Lambda$ CODE <br> SP  | RLVIII-96-2 | REV |
| :---: | :---: | :---: | :---: |
| DEC FORM NO |  | SHEET 6 | 6 |






























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R31 R 31

834 | R34 |
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| R32 | R33 ${ }_{6}^{Q}$ 05-08 09

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E $25 . E 36$ E25.E36
E58
E)1,
E35,E59
E4.F61

| E16 |
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| E5 |

E 39
E9,F55
E17
E26,E27,E33.E41,E42
E45 $27, E 56$
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